



#### features

- Camera Link® board for image acquisition
- PCIe x4 (quad lane)
- 512 MB RAM

#### general data

manufacturer	SiliconSoftware
manufacturer designation	PCIe-CL microEnable 5 marathon ACL

#### interface

bus	PCI Express
bus width	PCIe x4 (quad lane)
data interface	Camera Link®
PoCL support	yes
number of ports	2
ports	2x SDR26
transfer rate	Dual BASE, MEDIUM, FULL, 10-taps FULL, DECA (80bit)

#### environmental conditions

conformity	CE RoHS
------------	------------